

Serial No.: 10/501,530
Art Unit: 2621

PD020002
Customer No. 24498

Remarks/Arguments

The Office Action mailed October 3, 2008 has been reviewed and carefully considered.

Claims 6, 7 and 9 have been canceled without prejudice. Claims 1-5 have been amended. Claims 1-5 and 8 are now pending in this application.

Reconsideration of the above-identified application, as herein amended and in view of the following remarks, is respectfully requested.

Objection to the Specification

The Examiner has requested an amended title to clearly indicate the invention to which the claims are directed. In response, applicant has amended the title according to the Examiner's suggestion.

Claim objections

Claims 5 and 6 stand objected to because of the use of abbreviations in the same. Claims 6 and 7 have been canceled from the application. As such, this objection is now moot.

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Claim rejections

Claims 6 and 7 stand rejected under 35 U.S.C. §112, first paragraph, as failing to comply with the written description requirement. In response, applicant points out the noted acronyms that are the subject of claims 6 and 7 were originally disclosed at paragraphs 0007, 0009 as code sequences for the random access memory. It is respectfully asserted that these are known definitions/commands from datasheets corresponding to known SDRAMs. As such, one of skill in the art would undoubtedly understand what these commands are, and how to use the same. Applicant has amended the specification at paragraph 0007 and 0009 to include the known definitions of these very well known SDRAM command codes. In view of the cancellation of claims 6 and 7, this rejection is considered moot.

Claims 4, 6 and 7 stand rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 1 has been amended such that "the random access memory" has been recited. As such, claim 4 now conforms to antecedent basis rules. Claims 6 and 7 have been canceled. Reconsideration and withdrawal of the rejection is respectfully requested.

Claims 1-9 stand rejected under 35 U.S.C. §102(b) as being anticipated by USP 5,343,625 to Willis. Claim 1 has been amended to clarify the step of compressing the video signals. The method according to claim 1 recites that: the video signals to be stored are divided into a plurality of parallel data streams. Further Claim 1 recites that the data streams are time-compressed in a way that the compressed data streams take up only part of a predetermined write-cycle of the random access memory; and the data

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streams read from the random access memory are conducted via the second buffer memory and combined to form video signals.

The method according to claim 1 enables rapid reading and writing, so that even video signals with very high bit rates can be stored. Moreover, SDRAMs (very quick working Synchronous Dynamic Random Access Memories) with large capacities and low costs can be used.

Willis does not describe or suggest dividing video signals to be stored into a plurality of parallel data streams, whereby the data streams are time-compressed using a first buffer memory in a way that the compressed data streams take up only a part of a predetermined write cycle of a random access memory and combining the data streams read from the random access memory and conducted via a second buffer memory in order to form video signals.

Willis discloses synchronization of auxiliary video data with main video data by utilizing a random access memory as a field memory (350) and a first in first out line memory (354). The auxiliary video signal is delayed in the field memory and expanded in the line memory (Fig. 6, Col. 18, lines 41-49).

In addition, Willis discloses various steps of multiplexing in Figure 8. However, the above-mentioned line memory (354) is not connected to one of the multiplexer "MUX" (315, 317, 319, 321, Fig. 8) but to a "DEMUX" (355) for an inverted process.

It is respectfully asserted that the division of video signals into data stream and combining the data streams after storage according to claim 1, is neither disclosed nor remotely suggested by Willis.

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In view of the foregoing, Applicant respectfully requests that the rejections of the claims set forth in the Office Action of October 3, 2008 be withdrawn, that pending claims 1-5 and 8 be allowed, and that the case proceed to early issuance of Letters Patent in due course.

Conclusion

In view of the foregoing amendments to the claims and the accompany remarks, applicants solicits entry of this amendment and allowance of the claims. If, however, the Examiner cannot take such action, the Examiner should contact the applicant's attorney at (609) 734-6820, so that a mutually convenient date and time for a telephonic interview may be scheduled.

No fees are believed due with regard to this Amendment. Please charge and fee or credit any overpayment to Deposit Account No. 07-0832.

Respectfully submitted,
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